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Date: April 25, 2005

TO: Examiner Karlsen, Ernest F. (703) 872-9318
Art Unit: 2829

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Subject Matter: 10/ 688,418

Attorney Doc: 64,610-042A

Message:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Application of:

Madhav Datta

Group Art Unit: 2829

Serial No.: 10/688,418

Examiner: Karlsen, Ernest F.

Filed: Oct. 17, 2003

In Response to Office Action

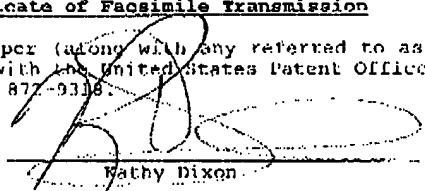
Dated: Jan. 25, 2005

For: Method for Testing Chips on Flat Solder Bumps

Attorney Docket No.: 64,610-042A (YO998-431US2)

Certificate of Facsimile Transmission

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Patent Office on the date shown below via facsimile no. (703) 877-9338.


Kathy Dixon

Date: Apr. 25, 2005

RESPONSE TO OFFICE ACTION

Commissioner for Patents
Alexandria, VA 22313-1450

Dear Sir:

In response to an Office Action mailed Jan. 25, 2005,
please consider the following remarks.